

ABSTRACT OF THE DISCLOSURE

An object of the present invention is to provide a simple and easily adjustable gate driving circuit for an active gate drive. As a configuration for this, a gate driving circuit includes a delay control signal creation unit
5 configured to create a delay control signal having a certain delay time with respect to a control signal given from a superior control device, a reference signal creation unit configured to create a voltage reference signal by waveform-shaping of the delay control signal, a voltage detector configured to detect a voltage between electrodes of a voltage-driven type switching
10 element and output a principal voltage detection signal, and a comparator configured to compare the principal voltage detection signal with the voltage reference signal and output a comparison result signal which controls a current source.